

## Electronic Acknowledgement Receipt

<b>EFS ID:</b>	2305244
<b>Application Number:</b>	10708700
<b>International Application Number:</b>	
<b>Confirmation Number:</b>	2699
<b>Title of Invention:</b>	SWITCHED CAPACITOR CIRCUIT CAPABLE OF MINIMIZING CLOCK FEEDTHROUGH EFFECT AND HAVING LOW PHASE NOISE AND METHOD THEREOF
<b>First Named Inventor/Applicant Name:</b>	En-Hsiang Yeh
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<b>Attorney Docket Number:</b>	MTKP0140USA
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### File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	1.501 Submission by Patent Owner	346494.PDF	107793 ee25dab4a7ab2baef7aded4eb45e32e316706286	no	1

### Warnings:

<b>Information:</b>					
2	Information Disclosure Statement Letter	MTKP0140USA0_D2_1_E04 01_1_NP063.PDF	102912	no	2
			e944c0b19093a6b87e59e39c01c7185f123cfee9		
<b>Warnings:</b>					
<b>Information:</b>					
3	Foreign Reference	jp6126324.pdf	324975	no	4
			6138662771c38d1ccca1b24e0c67da9ec20b67a6		
<b>Warnings:</b>					
<b>Information:</b>					
<b>Total Files Size (in bytes):</b>			535680		
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